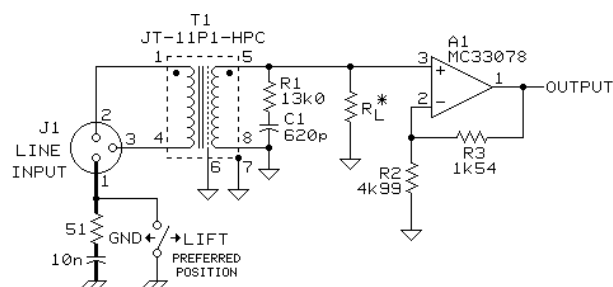


LINE INPUT TRANSFORMER

1:1 FOR "BALANCED BRIDGING" INPUTS

- Ideal for balancing any high-impedance unbalanced input
- Wide bandwidth: -3 dB at 0.25 Hz and 80 kHz
- Recommended for levels up to +19 dBu at 20 Hz
- High input impedance: 14 k Ω with 10 k Ω load
- High common-mode rejection: 124 dB at 60 Hz

This transformer is designed for use in wideband line input stages. Distortion remains very low and CMRR remains high, even when driven by high source impedances. The primary is fully balanced and its terminals may be reversed to invert polarity, if required. A 30 dB magnetic shield package is standard.

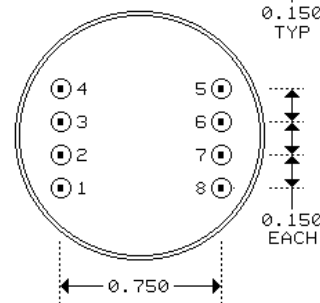
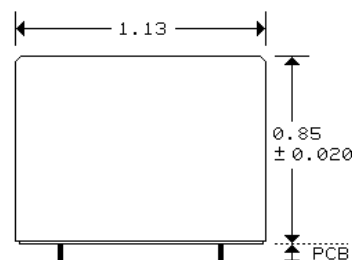
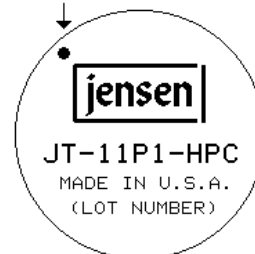


* R_L MUST BE 10 k Ω OR GREATER
OMIT DAMPING NETWORK R_1 AND C_1 FOR $R_L = 10$ k Ω ONLY
NO INTERNAL CONNECTION TO PINS 2 OR 3

LOW NOISE UNITY GAIN INPUT STAGE

TYPICAL APPLICATION

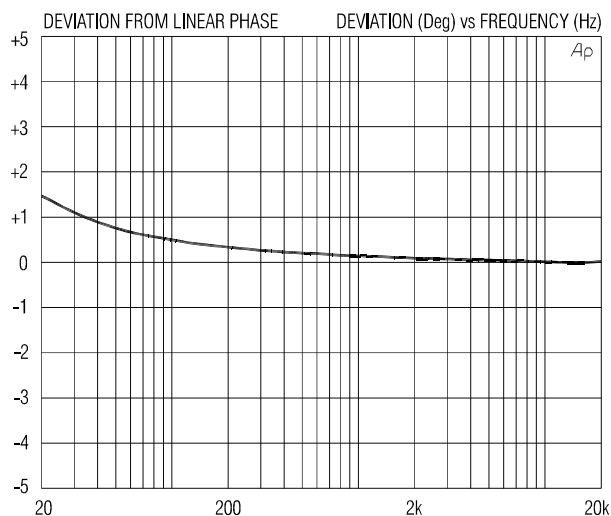
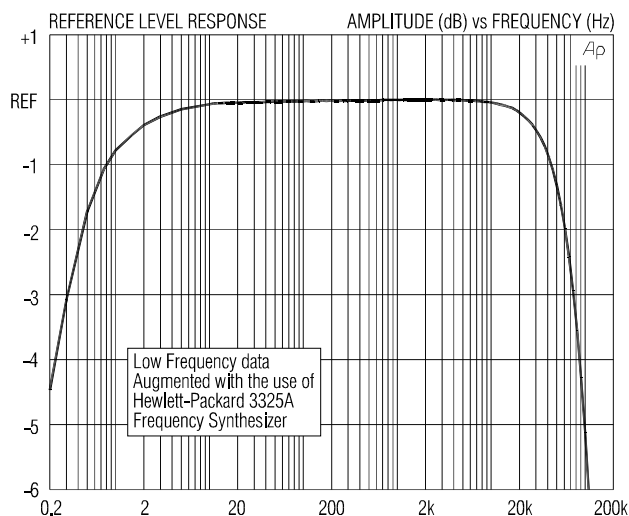
DOT INDICATES
PIN 1 BELOW

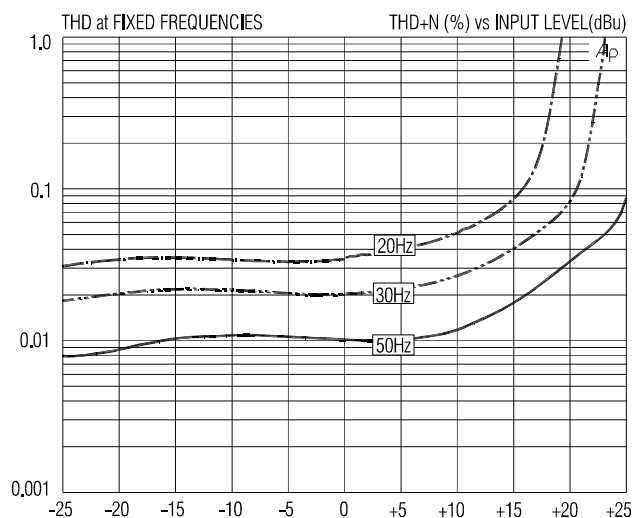
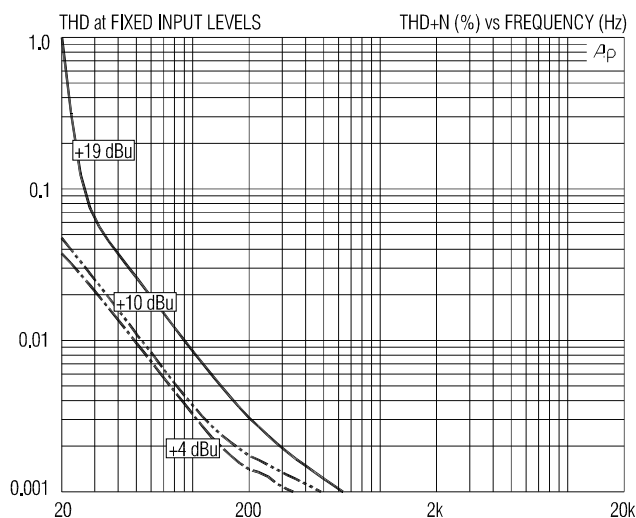


PIN NUMBERS NOT MARKED ON PART

TERMINAL PINS 0.018 x 0.030
PC HOLES 0.040 DIA SUGGESTED

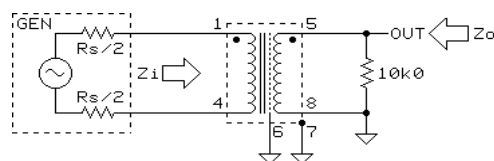
PART SUPPLIED WITH 5 MIL VALOX® INSULATOR
TO ALLOW PC TRACES UNDER TRANSFORMER



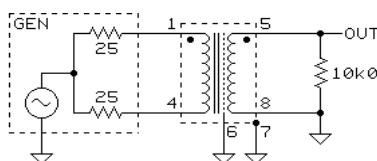


JT-11P-1HPC SPECIFICATIONS (all levels are input unless noted)

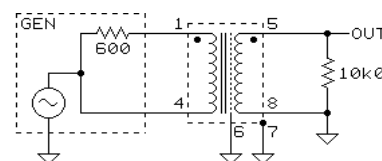
PARAMETER	CONDITIONS	MINIMUM	TYPICAL	MAXIMUM
Input impedance, Z_i	1 kHz, +4 dBu, test circuit 1	13.0 k Ω	14.1 k Ω	15.0 k Ω
Voltage gain	1 kHz, +4 dBu, test circuit 1	-3.2 dB	-3.0 dB	-2.8 dB
Magnitude response, ref 1 kHz	20 Hz, +4 dBu, test circuit 1, $R_s=600 \Omega$	-0.15 dB	-0.03 dB	± 0.0 dB
	20 kHz, +4 dBu, test circuit 1, $R_s=600 \Omega$	-0.35 dB	-0.20 dB	± 0.0 dB
Deviation from linear phase (DLP)	20 Hz to 20 kHz, +4 dBu, test circuit 1, $R_s=600 \Omega$		+1.4/-0°	$\pm 2.0^\circ$
Distortion (THD)	1 kHz, +4 dBu, test circuit 1, $R_s=600 \Omega$		<0.001%	
	20 Hz, +4 dBu, test circuit 1, $R_s=600 \Omega$		0.04%	0.10%
Maximum 20 Hz input level	1% THD, test circuit 1, $R_s=600 \Omega$	+17 dBu	+19 dBu	
Common-mode rejection ratio (CMRR) 600 Ω balanced source	60 Hz, test circuit 2		124 dB	
	3 kHz, test circuit 2	85 dB	95 dB	
Common-mode rejection ratio (CMRR) 600 Ω unbalanced source	60 Hz, test circuit 3		95 dB	
	3 kHz, test circuit 3		85 dB	
Output impedance, Z_o	1 kHz, test circuit 1, $R_s=600 \Omega$		3.2 k Ω	
DC resistances	primary (1 to 4)		2.26 k Ω	
	secondary (5 to 8)		1.90 k Ω	
Capacitances @ 1 kHz	primary to shield and case		105 pF	
	secondary to shield and case		115 pF	
Turns ratio		0.999:1	1.000:1	1.001:1
Temperature range	operation or storage	0° C		70° C
Breakdown voltage (see IMPORTANT NOTE below)	primary or secondary to shield and case, 60 Hz, 1 minute test duration	250 V RMS		



TEST CIRCUIT 1



TEST CIRCUIT 2



TEST CIRCUIT 3

All minimum and maximum specifications are guaranteed. Unless noted otherwise, all specifications apply at 25°C. Specifications subject to change without notice. All information herein is believed to be accurate and reliable, however no responsibility is assumed for its use nor for any infringements of patents which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Jensen Transformers, Inc.

IMPORTANT NOTE: This device is NOT intended for use in life support systems or any application where its failure could cause injury or death. The breakdown voltage specification is intended to insure integrity of internal insulation systems; continuous operation at these voltages is NOT recommended. Consult our applications engineering department if you have special requirements.

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